



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Appl. No. : 10/785,087
Applicant : Stephan Schröder et al.
Filed : February 25, 2004
TC/A.U. : 2811
Examiner : Unknown
Confirmation No. : 8539
Docket No. : 0928.0031C
Customer No. : 27896
Title : Integrated Semiconductor Circuit Having a
Multiplicity of Memory Cells

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT

Sir:

In response to the Office Action mailed May 27, 2005, please amend the
above-identified application as follows:

Amendments to the Claims are reflected in the listing of claims, which begins on
page 2 of this paper.

Remarks begin on page 5 of this paper.

OK
to
enter
MP
7/05